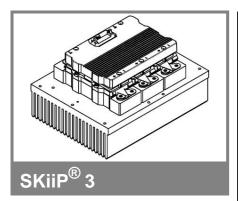
SKiiP 513GD172-3DUL



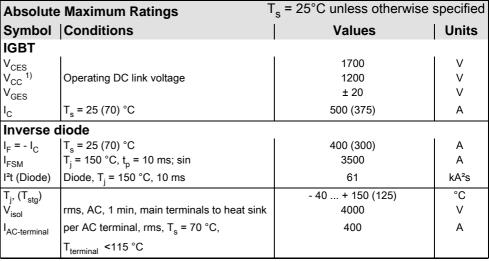
6-pack-integrated intelligent Power System

Power section SKiiP 513GD172-3DUL

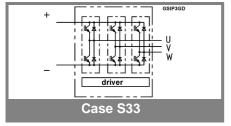
Data

Power section features

- SKiiP technology inside
- Trench IGBTs
- CAL diode technology
- Integrated current sensor
- Integrated temperature sensor
- Integrated heat sink
- IEC 60721-3-3 (humidity) class 3K3/IE32 (SKiiP® 3 System)
- IEC 60068-1 (climate) 40/125/56
- UL recognized file no. E63532
- 1) with assembly of suitable MKP capacitor per terminal



Characteristics			T _s = 25°C unless otherwise specified						
Symbol	ymbol Conditions				min.	typ.	max.	Units	
IGBT	•								
V _{CEsat}	I _C = 300 A, measured at term	Γ _j = 25 (´ minal	125) °C;			1,9 (2,2)	2,4	V	
V_{CEO}	$T_i = 25 (125)$) °C; at t	erminal			1 (0,9)	1,2 (1,1)	V	
r_{CE}	$T_i = 25 (125)$					3 (4,1)	3,9 (5)	mΩ	
I _{CES}	$V_{GE} = 0 \text{ V}, \text{ V}$ $T_i = 25 (125)$	/ _{CE} = V _C) °C	ES [,]			1,2 (72)		mA	
$E_{on} + E_{off}$	$I_{\rm C}^{\rm J}$ = 300 A, \		0 V			195		mJ	
	T _j = 125 °C,					288		mJ	
R _{CC+EE}	terminal chip	o, T _j = 25	5 °C			0,5		mΩ	
L_{CE}	top, bottom					12		nH	
C _{CHC}	per phase, A	AC-side				1,7		nF	
Inverse o									
$V_F = V_{EC}$	I _F = 300 A, The measured at terms	Γ _j = 25 (1 ^{minal}	25) °C			1,9 (1,7)	2,4	V	
V _{TO}	T _i = 25 (125) °C				1,1 (0,8)	1,4 (1,1)	V	
r _T	$T_j = 25 (125)$ $T_j = 25 (125)$) °C				2,6 (2,9)	3,4 (3,7)	mΩ	
E _{rr}	$I_{\rm C} = 300 \text{A}, \text{V}$	V _{CC} = 90	0 V			36		mJ	
	$T_j = 125 ^{\circ}C,$	V _{CC} = 1	200 V			43		mJ	
Mechani	cal data				•				
M_{dc}	DC terminal	s, SI Uni	ts		6		8	Nm	
M_{ac}	AC terminals	-			13		15	Nm	
W	SKiiP® 3 Sys	SKiiP® 3 System w/o heat sink				2,4			
W	heat sink	heat sink				7,5			
Thermal characteristics (PX 16 heat sink with fan SKF16B-230-1); "s" reference to heat sink; "r" reference to built-in temperature sensor (acc.IEC 60747-15)									
$R_{th(j-s)l}$	per IGBT						0,059	K/W	
R _{th(j-s)D}	per diode						0,115	K/W	
Z _{th}	R _i (mK/W) (ı	R _i (mK/W) (max. values)				tau _i (s)			
	1	2	3	4	1	2	3	4	
$Z_{th(j-r)I}$	10,2	28,8	21	0	363	0,18	0,04	1	
$Z_{\text{th(j-r)D}}$	36	36	54	60	30	5	0,25	0,04	



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1,4

210

85

11

0,4

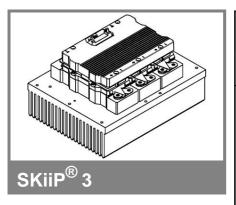
20

5,5

 $Z_{th(r-\underline{a})}$

2,1

SKiiP 513GD172-3DUL



6-pack-integrated intelligent Power System

6-pack integrated gate driver SKiiP 513GD172-3DUL

Data

Gate driver features

- CMOS compatible inputs
- Wide range power supply
- Integrated circuitry to sense phase current, heat sink temperature and

DC-bus voltage (option)

- Short circuit protection
- Over current protection
- Over voltage protection (option)
- Power supply protected against under voltage
- · Interlock of top/bottom switch
- Isolation by transformer
- IEC 60068-1 (climate) 40/85/56
- UL recognized file no. 242581

Absolute	Maximum Ratings	Γ _a = 25°C unless otherwise specified		
Symbol	Conditions	Values	Units	
V_{S2}	unstabilized 24 V power supply	30	V	
V_{i}	input signal voltage (high)	15 + 0,3	V	
dv/dt	secondary to primary side	75	kV/μs	
V_{isollO}	input / output (AC, rms, 2s)	4000	V	
V _{isoIPD}	partial discharge extinction voltage, rms, Q _{PD} ≤10 pC;	1500	V	
V _{isol12}	output 1 / output 2 (AC, rms, 2s)	1500	V	
f _{sw}	switching frequency	14	kHz	
f _{out}	output frequency for I _{peak(1)} =I _C	14	kHz	
$T_{op} (T_{stg})$	operating / storage temperature	- 40 + 85	°C	

Characte	eristics	(T _a = 25°C			
Symbol	Conditions	min.	typ.	max.	Units
V_{S2}	supply voltage non stabilized	13	24	30	V
I _{S2}	V _{S2} = 24 V	420+34*f/kHz+0,00015*(I _{AC} /A) ²			mA
V _{iT+}	input threshold voltage (High)			12,3	V
V_{iT-}	input threshold voltage (Low)	4,6			V
R _{IN}	input resistance		10		kΩ
C _{IN}	input capacitance		1		nF
t _{d(on)IO}	input-output turn-on propagation time		1,3		μs
$t_{d(off)IO}$	input-output turn-off propagation time		1,3		μs
tpERRRESET	error memory reset time		9		μs
t_{TD}	top / bottom switch interlock time		3		μs
I _{analogOUT}	max. 5mA; 8 V corresponds to 15 V supply voltage for external components		500		Α
I _{s1out}	max. load current			50	mA
I _{TRIPSC}	over current trip level (I _{analog} OUT = 10 V)		625		Α
T_tp	over temperature protection	110		120	°C
UDCTRIP	U _{DC} -protection (U _{analog OUT} = 9 V); ()		1200		V

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